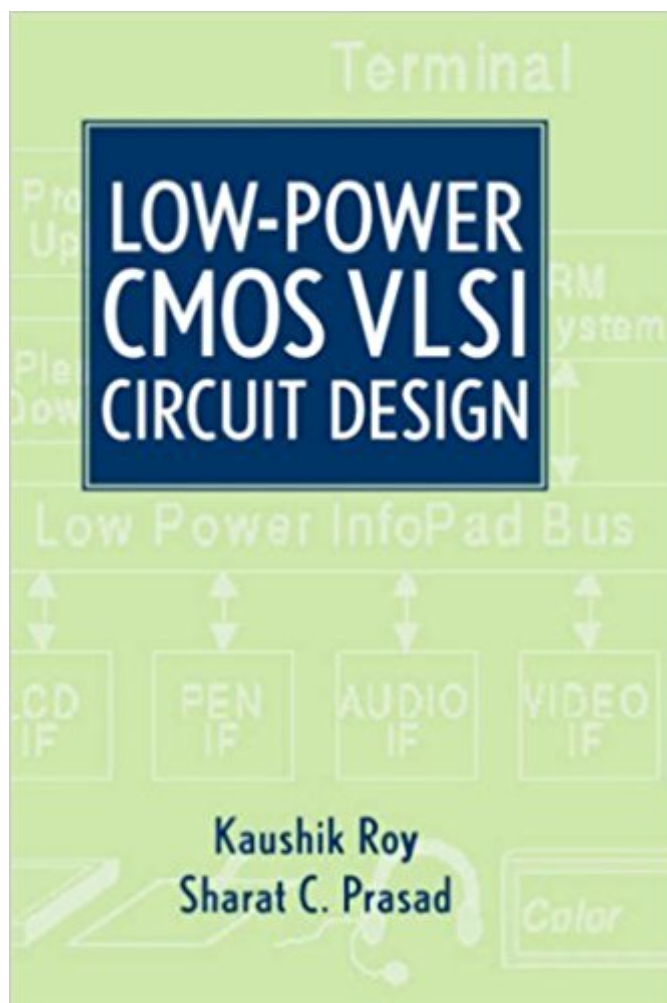


The book was found

# Low-Power CMOS VLSI Circuit Design



## Synopsis

A comprehensive look at the rapidly growing field of low-power VLSI design. Low-power VLSI circuit design is a dynamic research area driven by the growing reliance on battery-powered portable computing and wireless communications products. In addition, it has become critical to the continued progress of high-performance and reliable microelectronic systems. This self-contained volume clearly introduces each topic, incorporates dozens of illustrations, and concludes chapters with summaries and references. VLSI circuit and CAD engineers as well as researchers in universities and industry will find ample information on tools and techniques for design and optimization of low-power electronic systems. Topics include: \* Fundamentals of power dissipation in microelectronic devices \* Estimation of power dissipation due to switching, short circuit, subthreshold leakage, and diode leakage currents \* Design and test of low-voltage CMOS circuits \* Power-conscious logic and high-level synthesis \* Low-power static RAM architecture \* Energy recovery techniques \* Software power estimation and optimization

## Book Information

Hardcover: 376 pages

Publisher: Wiley-Interscience; 1 edition (February 22, 2000)

Language: English

ISBN-10: 047111488X

ISBN-13: 978-0471114888

Product Dimensions: 6.4 x 0.9 x 9.7 inches

Shipping Weight: 1.6 pounds (View shipping rates and policies)

Average Customer Review: 4.6 out of 5 stars 3 customer reviews

Best Sellers Rank: #370,163 in Books (See Top 100 in Books) #13 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > VLSI & ULSI](#) #47 in [Books > Engineering & Transportation > Engineering > Electrical & Electronics > Circuits > Integrated](#) #57 in [Books > Computers & Technology > Hardware & DIY > Microprocessors & System Design > Computer Design](#)

## Customer Reviews

"This is a highly recommended book for all academic engineering libraries." (E-Streams, Vol. 4, No. 8, August 2001)

A comprehensive look at the rapidly growing field of low-power VLSI design. Low-power VLSI circuit

design is a dynamic research area driven by the growing reliance on battery-powered portable computing and wireless communications products. In addition, it has become critical to the continued progress of high-performance and reliable microelectronic systems. This self-contained volume clearly introduces each topic, incorporates dozens of illustrations, and concludes chapters with summaries and references. VLSI circuit and CAD engineers as well as researchers in universities and industry will find ample information on tools and techniques for design and optimization of low-power electronic systems. Topics include: Fundamentals of power dissipation in microelectronic devices Estimation of power dissipation due to switching, short circuit, subthreshold leakage, and diode leakage currents Design and test of low-voltage CMOS circuits Power-conscious logic and high-level synthesis Low-power static RAM architecture Energy recovery techniques Software power estimation and optimization

The most upto date book on low-power design methods. Some information is redundant and can be eliminated from the book. The chapter on the physics of a transistor is excellent. THERE ARE A LOT OF TYPOS IN THE BOOK!

This book was very helpful for me to understand about modern low-power chip design,also made me save my times to search the papers about this topic. Great book.

It is the most comprehensive book in the low power CMOS design area, which covers the low power methods in different levels.

[Download to continue reading...](#)

Low Carb: 365 Days of Low Carb Recipes (Low Carb, Low Carb Cookbook, Low Carb Diet, Low Carb Recipes, Low Carb Slow Cooker, Low Carb Slow Cooker Recipes, Low Carb Living, Low Carb Diet For Beginners) Low-Power CMOS VLSI Circuit Design Low Carb Diet: Introduction To Low Carb Diet And Recipes Of Low Carb Soups And Casseroles: (low carbohydrate, high protein, low carbohydrate foods, low carb, low carb cookbook, low carb recipes) Low Carb Cookbook: Delicious Snack Recipes for Weight Loss. (low carbohydrate foods, low carb cooking, low carb diet, low carb recipes, low carb, low carb ... dinner recipes, low carb diets Book 1) Low Carb Candy Bars: 25 Low Carb Recipes To Satisfy Your Sweet Tooth: (low carbohydrate, high protein, low carbohydrate foods, low carb, low carb cookbook, low carb recipes) Integrated circuit devices and components (Integrated-circuit technology, analog and logic circuit design, memory and display devices) Low Carb Cookbook: 500 BEST LOW CARB RECIPES (low carb diet for beginners, lose weight, Atkins

diet, low carb foods, low carb diet weight loss, low carb food list) Keto Bread Cookbook: Real Low Carb Recipes: (low carbohydrate, high protein, low carbohydrate foods, low carb, low carb cookbook, low carb recipes) The Ketogenic Diet Cookbook: Lose 15 Lbs In Two-Weeks With 66 Perfect Low Carb Keto Recipes: (low carbohydrate, high protein, low carbohydrate foods, low carb, low carb cookbook, low carb recipes) Low Carb: The Ultimate Beginner's Low Carb Guide to Lose Weight Quick without Starving With over 20 Easy Recipes To Follow. (Low Carb, Low Carb Cookbook, ... Diet, Low Carb Recipes, Low Carb Cookbook) Winter Circuit (Show Circuit Series -- Book 2) (The Show Circuit) VLSI DESIGN SIMPLE AND LUCID EXPLANATION: vlsi design for students CMOS VLSI Design: A Circuits and Systems Perspective (4th Edition) CMOS VLSI Design: A Circuits and Systems Perspective CMOS VLSI Design: A Circuits and Systems Perspective (3rd Edition) Nanoscale CMOS VLSI Circuits: Design for Manufacturability Chip Design for Submicron VLSI: CMOS Layout and Simulation Principles of CMOS VLSI Design CMOS Analog Circuit Design (The Oxford Series in Electrical and Computer Engineering) CMOS Circuit Design, Layout, and Simulation, 3rd Edition (IEEE Press Series on Microelectronic Systems)

[Contact Us](#)

[DMCA](#)

[Privacy](#)

[FAQ & Help](#)